

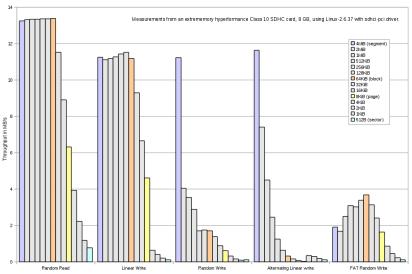
Linaro Itd.

Working with cheap flash drives

Arnd Bergmann

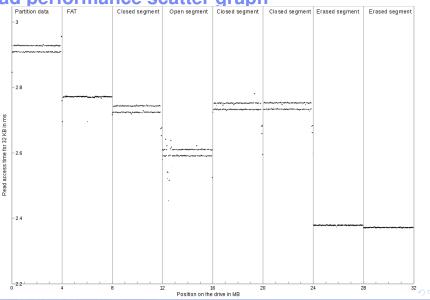
© 2010 IBM Corporation

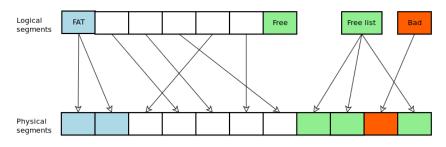
Performance footprint of an SD card



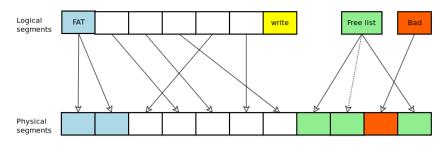


Read performance scatter graph

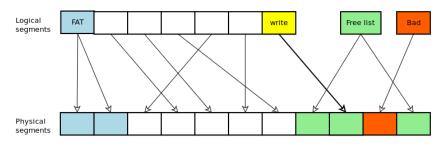




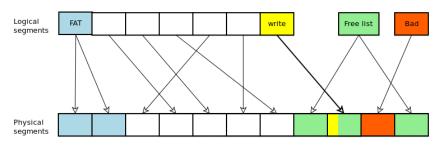
Writing to a free segment



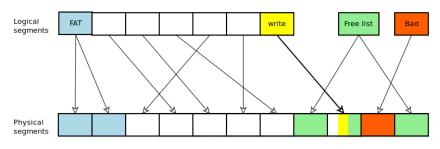
Writing to a free segment



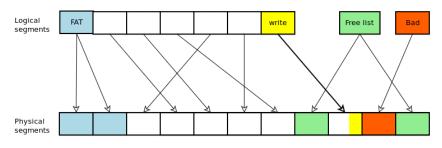
Writing to a free segment



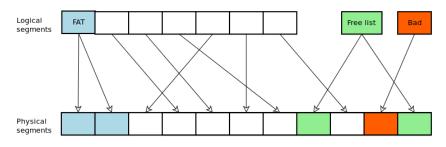
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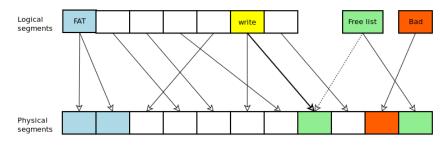
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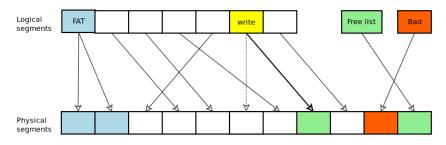
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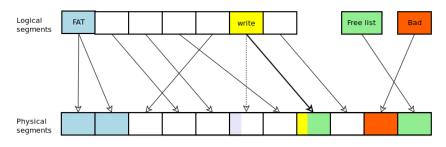
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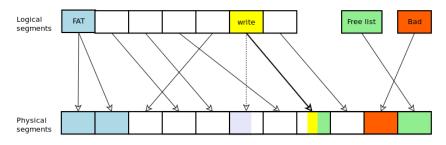
Overwriting a used logical segment



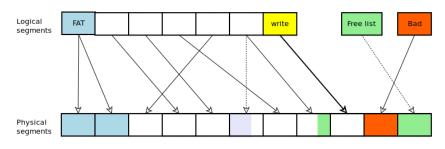
Overwriting a used logical segment



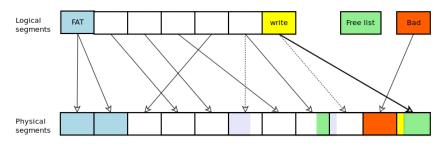
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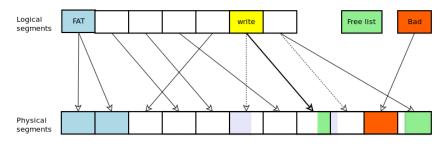
Overwriting a used logical segment



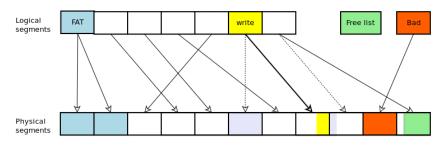
Writing to multiple segments



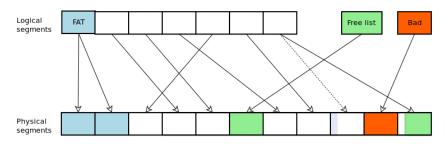
Writing to multiple segments



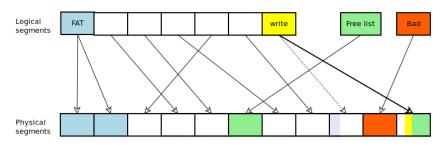
Writing to multiple segments

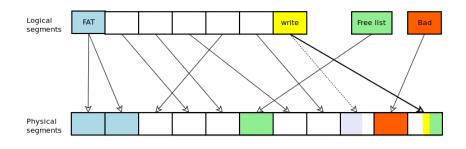


Writing to multiple segments

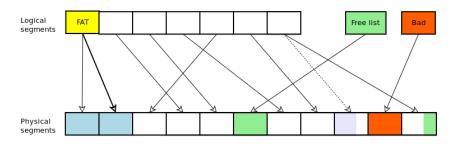


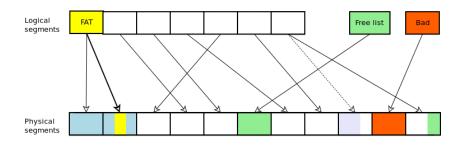
Writing to multiple segments



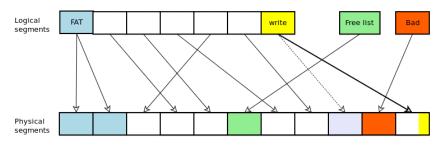


Concurrent FAT updates

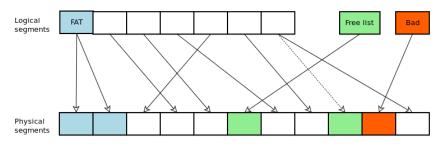




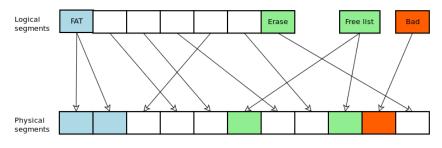
Concurrent FAT updates



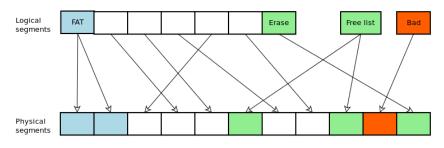
Concurrent FAT updates



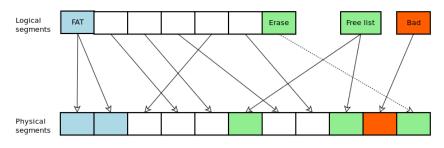
Concurrent FAT updates



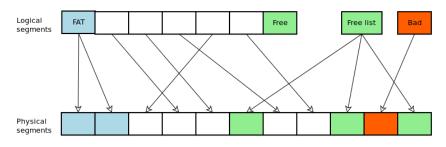
Erasing a segment



Erasing a segment

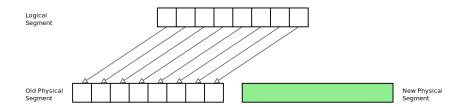


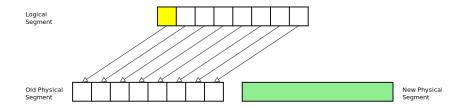
Erasing a segment

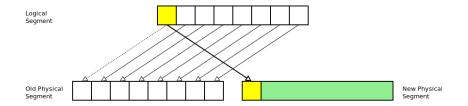


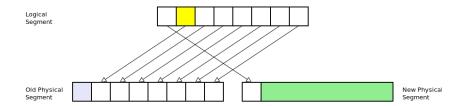
Erasing a segment

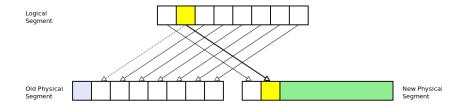


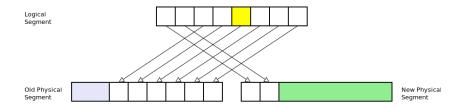


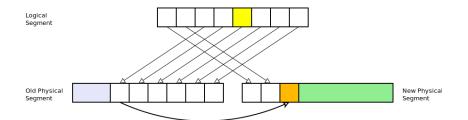


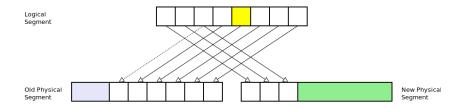


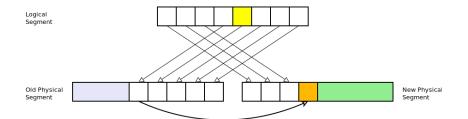


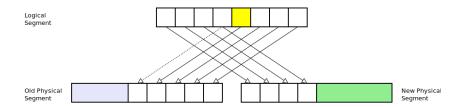


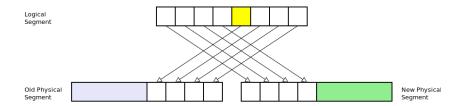


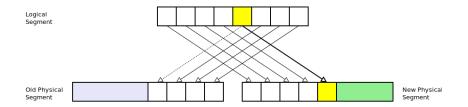


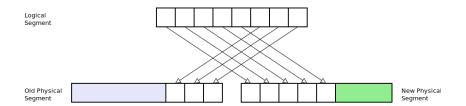


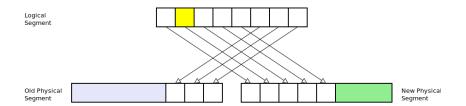


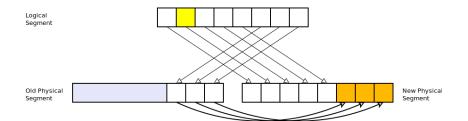


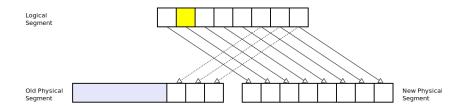


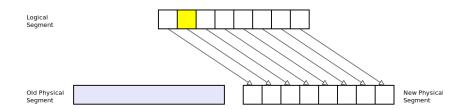


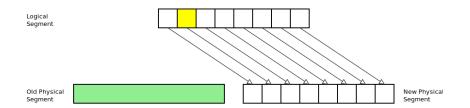


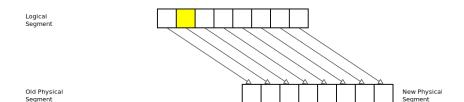






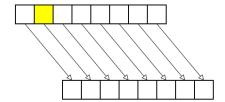






Logical Segment

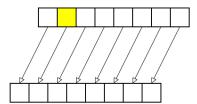
Old Physical Segment



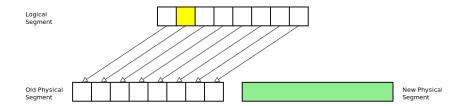
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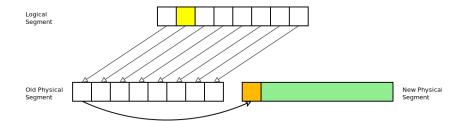
Logical Segment

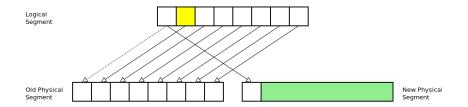
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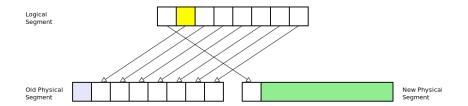


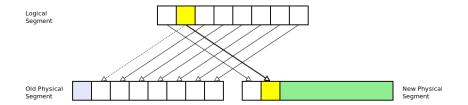
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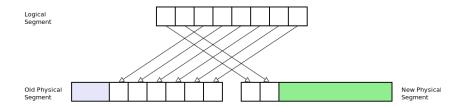


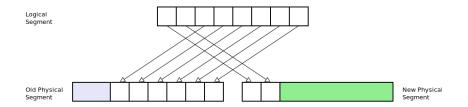




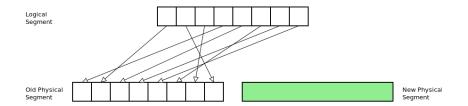


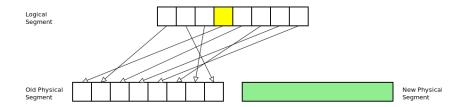


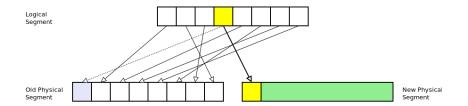


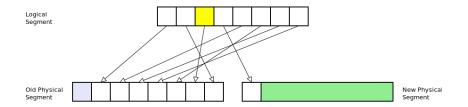


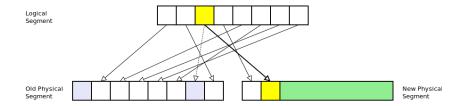


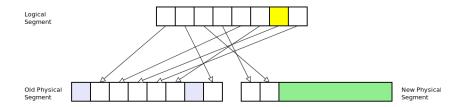


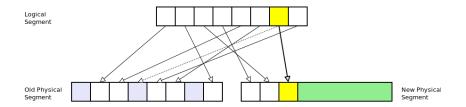


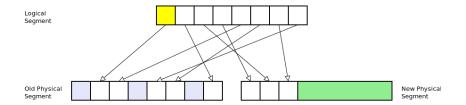


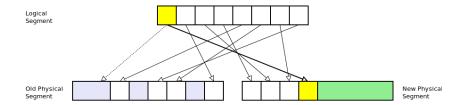


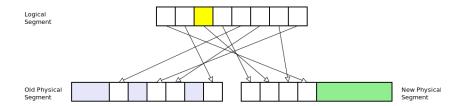


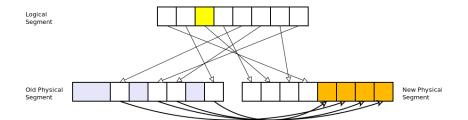


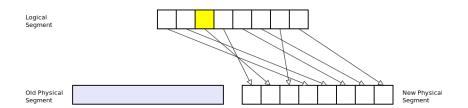


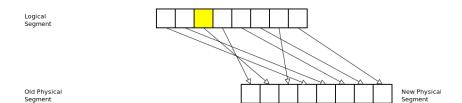






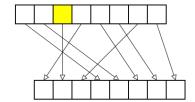




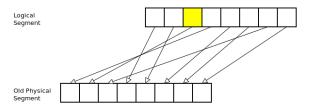


Logical Segment

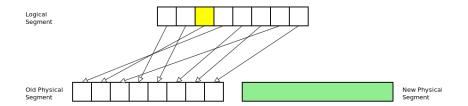
Old Physical Segment

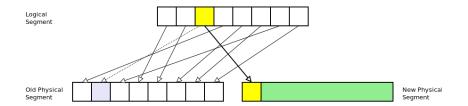


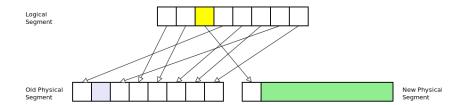
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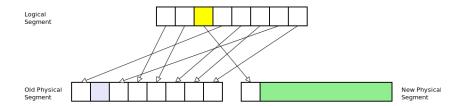


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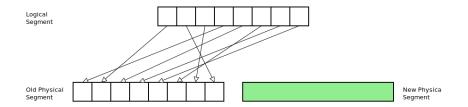


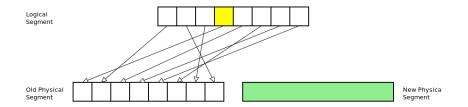


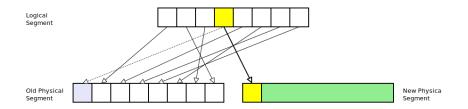


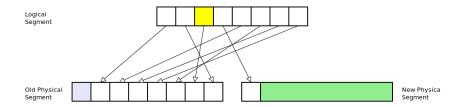


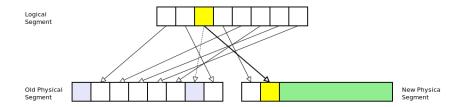


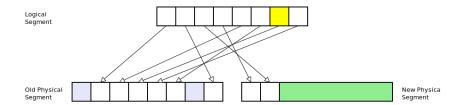


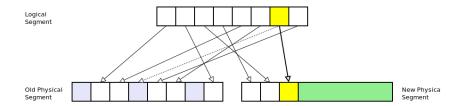


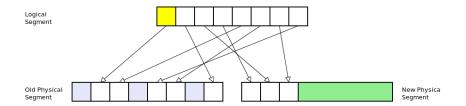


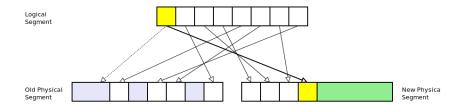


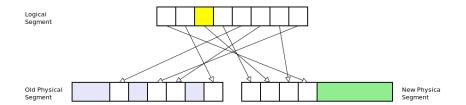


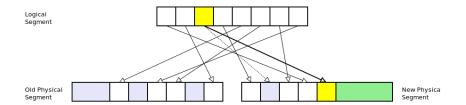


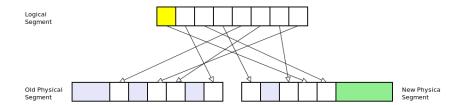


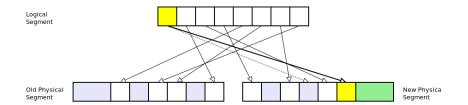


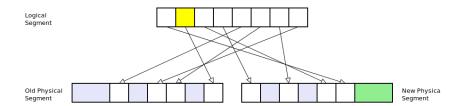


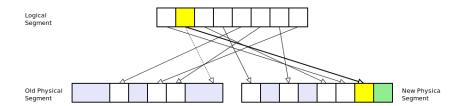


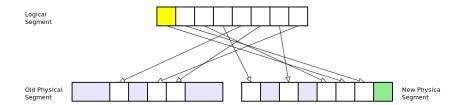


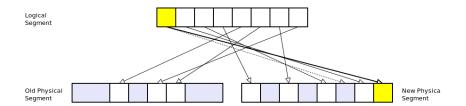


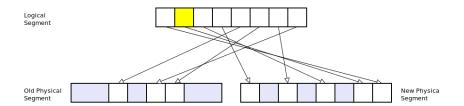


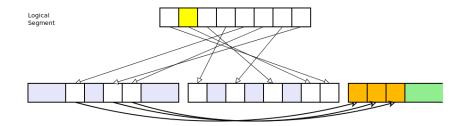


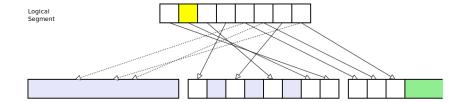


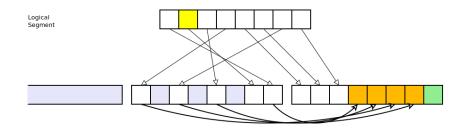


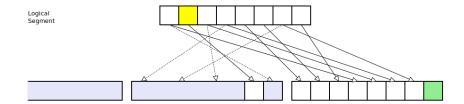


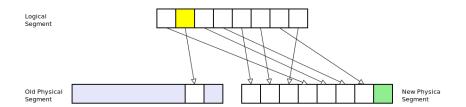


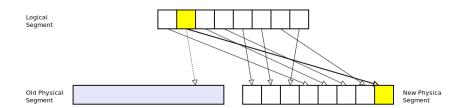


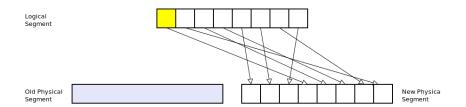


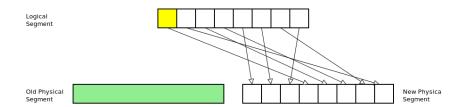


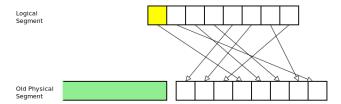




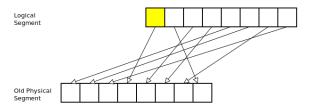




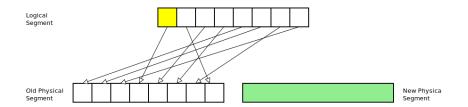


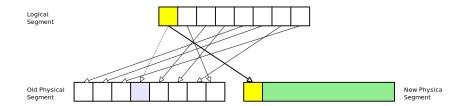


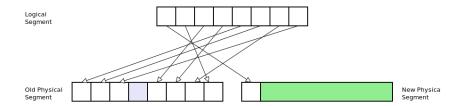
New Physica Segment

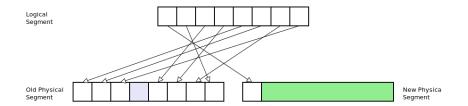


New Physica Segment

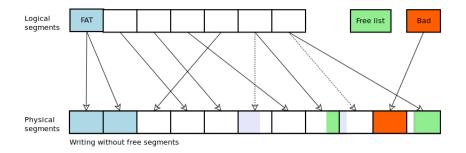


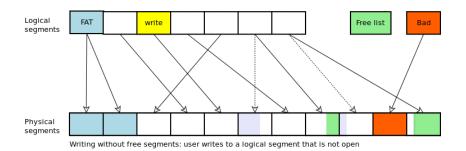


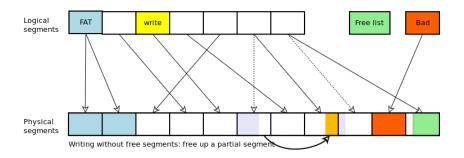


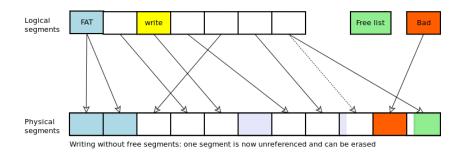


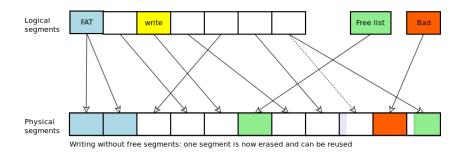


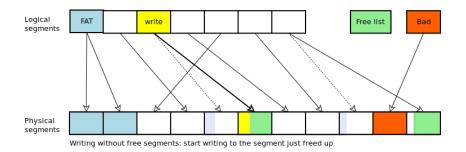


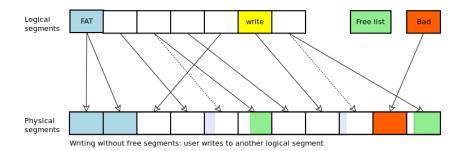


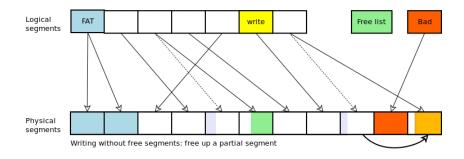


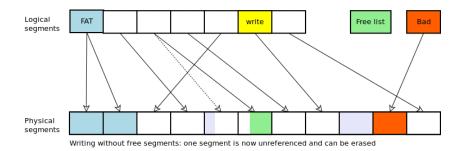


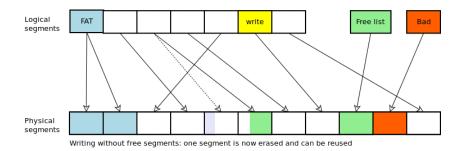


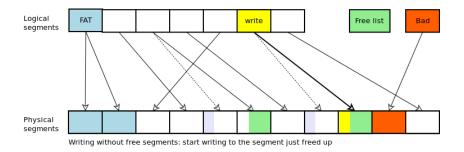


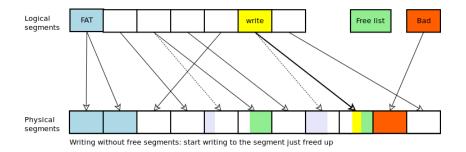


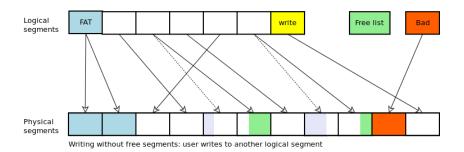


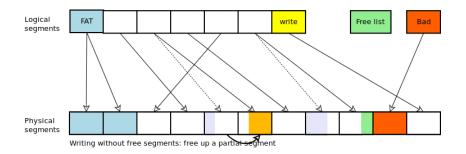


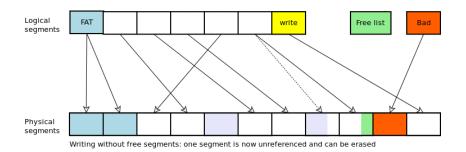


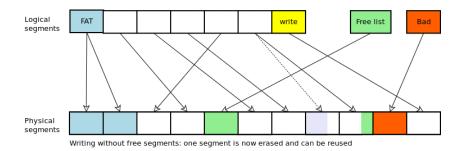


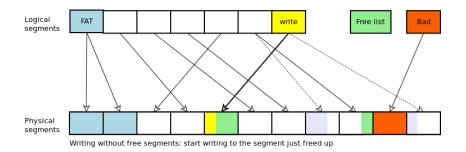


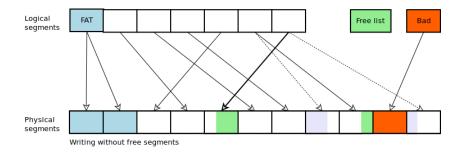




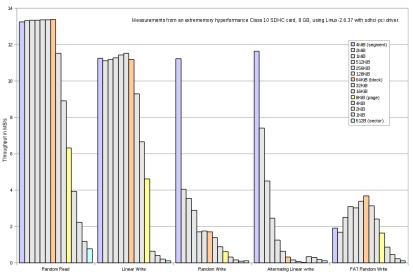








Performance footprint of an SD card





Flashbench demo

Superpage buffer cache



- Superpage buffer cache
- Flashcache device mapper target

- Superpage buffer cache
- Flashcache device mapper target
- Erase block optimizations in elevator

- Superpage buffer cache
- Flashcache device mapper target
- Erase block optimizations in elevator
- File system block allocation

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