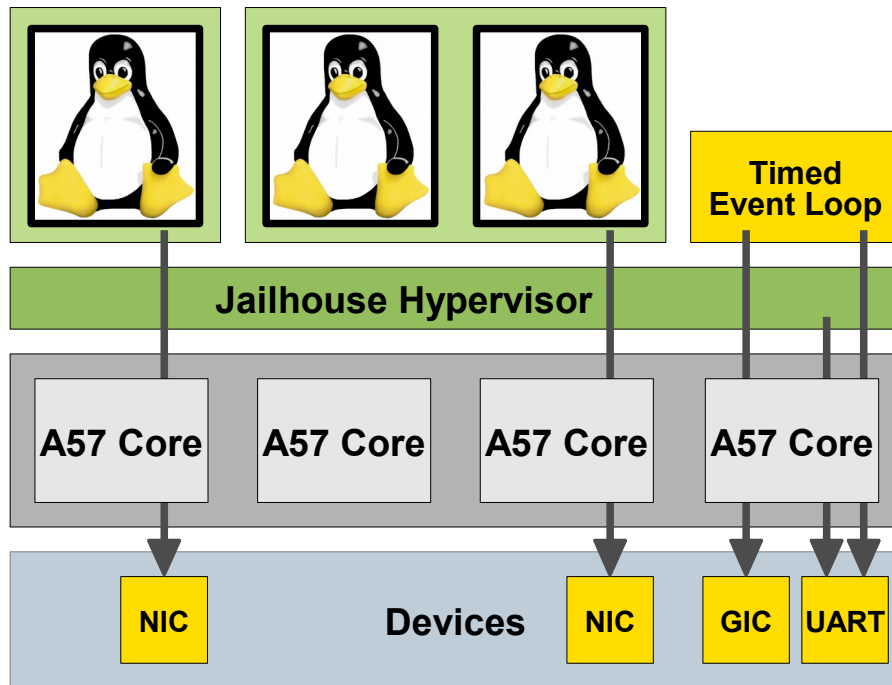# Partitioning Hypervisor Jailhouse on ARM64

Jan Kiszka, Siemens AG

What is demonstrated

What was improved

Huawei Research Center Munich
  ported Jailhouse to AArch64

Minimal changes to hypervisor core required
  => Jailhouse architecture well portable

Can run multiple partitions aside primary Linux
• Bare-metal
• RTOSes
• unpatched Linux

Ongoing code review and patch series integration

WiP / future AArch64 targets
• HiKey (Kirin 620)
• Xilinx ZynqMP (safety scenarios)

Thanks to AMD for providing development systems!

Source code or detail technical information availability

https://github.com/siemens/jailhouse
Branches: master, wip/arm64

Hardware Information

AMD Opteron A1100, 8-core Cortex A57
32 GB RAM, 2x 10G Ethernet, 8x SATA 3.0
SoftIron Overdrive 3000 development system