Partitioning Hypervisor Jailhouse on ARM64
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What is demonstrated

- Huawei Research Center Munich ported Jailhouse to AArch64
- Minimal changes to hypervisor core required => Jailhouse architecture well portable
- Can run multiple partitions aside primary Linux
  - Bare-metal
  - RTOSes
  - unpatched Linux
- Ongoing code review and patch series integration
- WiP / future AArch64 targets
  - HiKey (Kirin 620)
  - Xilinx ZynqMP (safety scenarios)

Hardware Information
AMD Opteron A1100, 8-core Cortex A57
32 GB RAM, 2x 10G Ethernet, 8x SATA 3.0
SoftIron Overdrive 3000 development system

Source code or detail technical information availability
https://github.com/siemens/jailhouse
Branches: master, wip/arm64