



Building an IoT Empire

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What is demonstrated

This hardware and software configuration demonstrates (1) low power (2) IoT relevant (3) networking of (4) embedded computing devices.

What was improved

- 1) Interop between milliwatt grade devices unable to run a monolithic kernel and Linux peers.
- 2) Facility of IoT connectivity, with a low threshold of user configuration and sophistication required.
- 3) Compliance with open networking protocols like MQTT, AMQP, and CoAP, as well as IoTivity (OCF participation.)
- 4) Convincing proof that embedded sensors, actuators, and computers are relevant to industry and consumer alike.

Hardware Information

ARM Cortex (M0, M0+, M4, A7, A8, A53) and Intel (IA32, Quark, ARC) based MCUs along with sensors and actuators

Source code or detail technical information availability

<https://edu-europalab.rhcloud.com/>
<https://ultimatecoder.intel.com/team-four/>