

Who Am I?

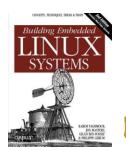
- I'm Gilad ben-Yossef.
- I'm a principal Software Engineer at Arm.
- I work on applied cryptography and security of the upstream Linux kernel in general and maintain the arm® TrustZone® CryptoCell® Linux device driver.
- I have been working in various forms with and on the Linux kernel and other Open Source projects for over twenty years.
- I have co-authored "Building Embedded Linux Systems" 2nd edition from O'Reilly.



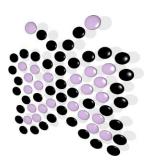








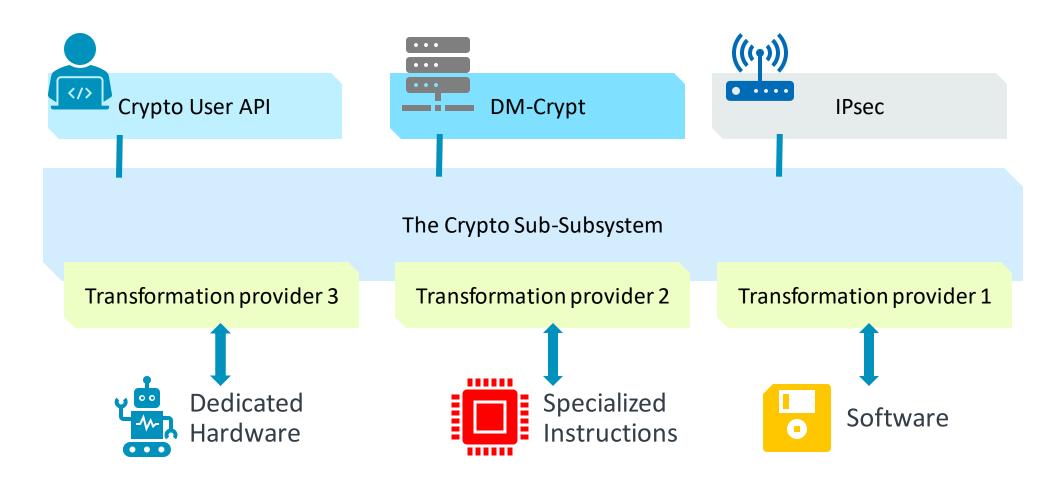






The Linux Cryptography Sub-System

Or the Linux Crypto API, in short





Crypto API Usage Example

```
tfm = crypto alloc skcipher("xts(aes)", 0, 0); // Get a handle of a transformation that handles XTS mode of AES.
err = crypto_skcipher_setkey(tfm, key, sizeof(key)); // Set the key to be used for all subsequent operations
req = skcipher_request_alloc(tfm, GFP_KERNEL); // Get a request handle
skcipher_request_set_callback(req, CRYPTO TFM REQ MAY BACKLOG | CRYPTO TFM REQ MAY SLEEP,
                               crypto req done, &wait); // Set the callback function to be called when done
skcipher_request_set_crypt(req, &sg, &sg, datasize, iv); // Set the input, output and initial vector buffers
ret = crypto_skcipher_encrypt(req); // Start the operation
err = crypto_wait_req(ret, &wait); // Wait for the operation to finish
crypto_free_skcipher(tfm); // Free things up
skcipher request free(req);
```



/proc/crypto

Generic name gby@gby:~\$ cat /proc/crypto : crc32 name Driver's name : crc32-pclmul driver : crc32 pclmul module **Priority** : 200 priority refcnt : 1 selftest : passed : shash type blocksize : 1

: xts(aes) name xts-aes-aesni driver module aesni_intel 400 priority refcnt : 1 selftest : passed : ablkcipher type async : yes blocksize : 16 min keysize : 32 max keysize : 64 ivsize : 16

geniv

: <default>

: 4

digestsize

Wait, back up a little...

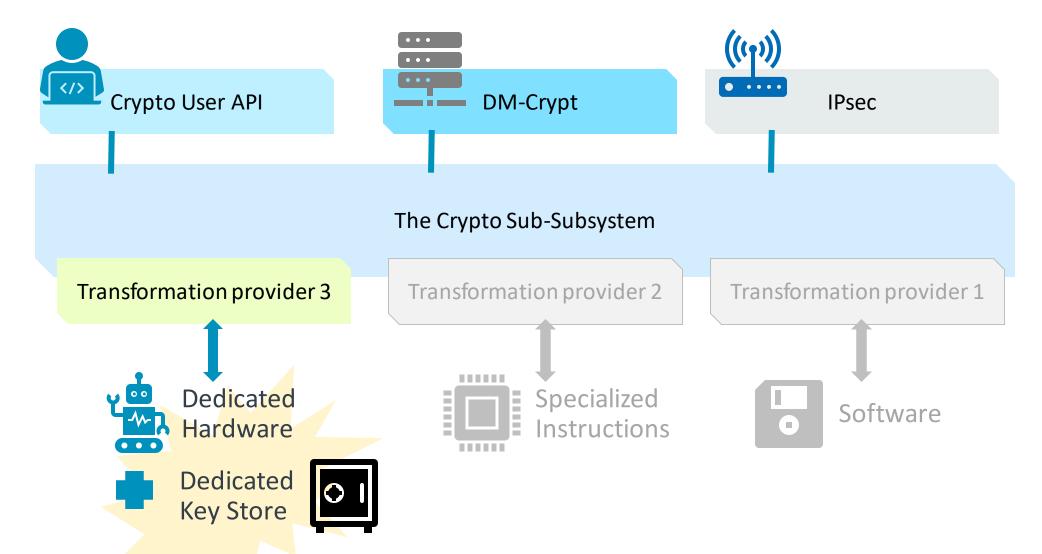
```
// Set the key to be used for all subsequent operations
err = crypto_skcipher_setkey(tfm, key, sizeof(key));
```

... where is the key stored? In RAM, like everything else.





Hardware Protected keys





From Big Iron to much smaller embedded iron

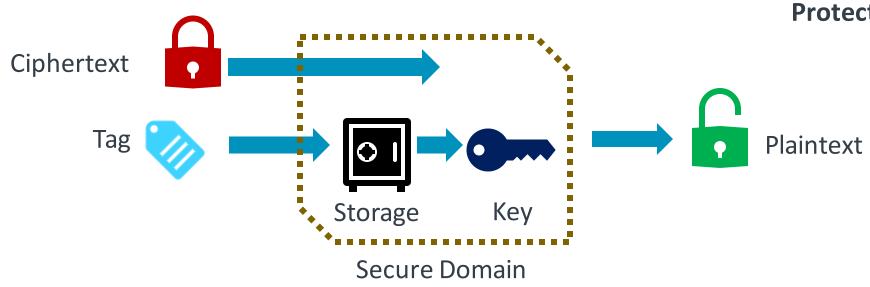




Protected Keys Usage



Plain Key Decryption

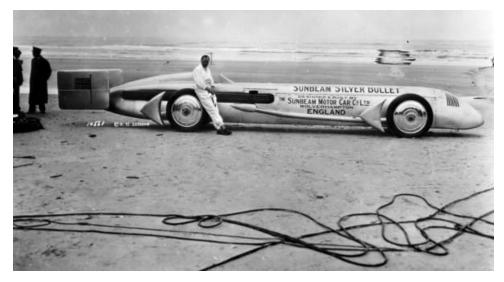


Protected Key Decryption



Not a silver bullet

- Actual security depends on the security of the so called "Secure Domain"...
- Ability to use the key might be enough for attacker.
- Key provisioning and management is a problem as always.
- A good component in a "Defense in Depths" strategy.



This work is from the <u>Florida Memory Project</u> hosted at the <u>State Archive of Florida</u>, and is released to the <u>public domain</u> in the United States under the terms of <u>Section 257.35(6)</u>, <u>Florida Statutes</u>.



Interface Details

Note: some parts are Arm CryptoCell specific

- The letter "p" is used as prefix to the generic algorithms name.
 - E.g. Use "paes" for Protected Key AES
- Because the tag value are implementation specific, use of a driver specific name is preferred over generic name, where possible.
 - E.g. Use "xts-paes-ccree" instead of "xts(paes)"
- Instead of the normal key, provide a tag appropriate for the implementation
 - E.g.

```
struct cc_hkey_info {
  u16 keylen;     /* Length of actual key in bytes*/
  u8 hw_key1;     /* First key index */
  u8 hw_key2;     /* Second key index (optional) */
} __packed;

#define CC HW KEY SIZE sizeof(struct cc hkey info)
```



Example: DM-Crypt with protected keys

Note: cryptsetup can also be used if you want to password protect the key index with PKDF



arm

Thank You

Danke

Merci

射射 ありがとう

Gracias

Kiitos

감사합니다

धन्यवाद

شکرًا

תודה

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