

#### **Technical Overview**

11 October 2016



#### **Problem and Context**

First release of a successful connected product...

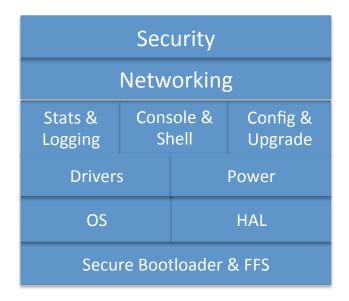


.... now make that repeatable please.



## An Open Source OS for MCUs

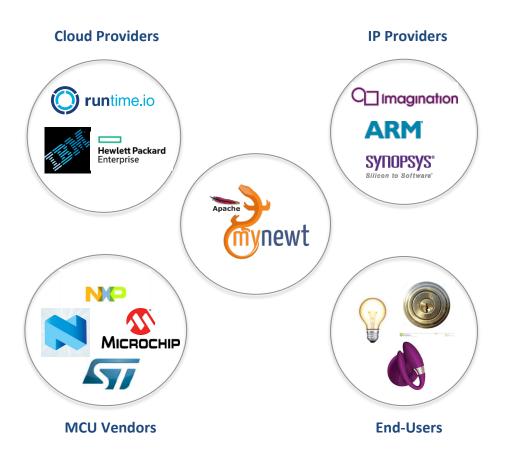




- Open source networking stacks:
   Bluetooth Host and Controller
- Pre-emptive, tickless RTOS with power management
- Secure bootloader and Image Upgrade
- Flash FS and Access Mechanisms
- Build & Package Management
- Management Interfaces



#### A Community Driven OS



#### Why Apache Software Foundation?

- Liberal, BSD-style license
- Strong Licensing and IP policies
- Meritocracy
- Free to contribute, contributors control project direction.
- History of working with large organizations: IBM, Pivotal/ EMC, Microsoft.
- Many years experience managing large, complex projects (e.g., Apache, Hadoop, Subversion)

Community driven open source best way to maintain healthy ecosystem



#### **RTOS**

- Tickless operation: low power hooks
- Driver Interface
- Pre-emptive, multitasking RTOS
  - Strict priority-based scheduling
  - Up to 253 different priority levels
- Unified buffer management
- Resource utilization tracking and watchdog
- High-resolution timers
- Built-in tasks:
  - Idle

```
truct os task task1;
os_stack_t stack1[TASK1_STACK_SIZE];
 tatic volatile int g task1 loops;
task1_handler(void *arg)
       ++g_task1_loops;
       os_time_delay(1000);
main(int argc, char **argv)
   int rc;
   os_init();
   os_task_init(&task1, "task1", task1_handler, NULL,
           TASK1 PRIO, OS WAIT FOREVER, stack1, TASK1 STACK SIZE);
   os_start();
   assert(0);
```





#### **RTOS - Event Driven Model**

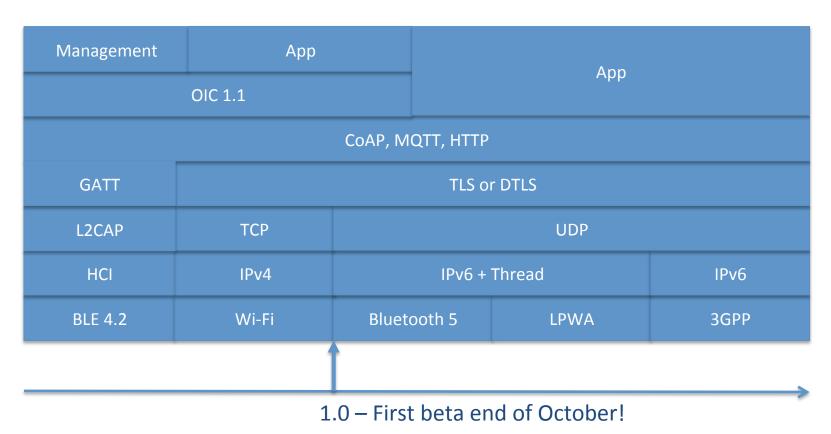
- Event Queues provide a mechanism for "mostly-sleeping" asynchronous tasks
- Wake-up on:
  - Message from another Task
  - Timer
  - I/O state change
  - Incoming packet
  - Watchdog
- Perform operations:
  - Send an alert
  - Respond to a request
  - Schedule a wakeup
- Go back to sleep



```
truct os eventg task1 evg;
truct os eventq task2 evq;
task1 handler(void *arg)
   struct os event *ev;
   struct os event ping ev;
   ping ev.ev type = OS EVENT T PING;
   ping_ev.ev_arg = NULL;
   os_eventq_put(&task2_evq, &ping_ev);
       ev = os_eventq_get(&task1_evq);
       assert(ev->ev_type == OS_EVENT_T_PONG);
       os eventg put(&task2 evg, &ping ev);
        ++g_task1_loops;
       os time delay(1000);
task2 handler(void *arg)
   struct os event *ev;
   struct os_event pong_ev;
   pong_ev.ev_type = OS_EVENT_T_PONG;
   pong_ev.ev_arg = NULL;
       ev = os_eventq_get(&task2_evq);
       assert(ev->ev_type == OS_EVENT_T_PING);
       os_eventq_put(&task1_evq, &pong_ev);
        ++g task2 loops;
```



# Apache Mynewt Connectivity Layer (Runtime's View)







## Highlights: Apache Mynewt Networking

Bluetooth 4.2

Wi-Fi



- Nordic nRF51 and nRF52 support
- Simultaneous Central and Peripheral modes
  - Supports up to 32 connections
- Combined (host + controller), hostonly and controller-only mode
- Compatible with BlueZ
- 40% less code versus licensed binaries in peripheral-only mode
- Abstracted interface to Wi-Fi supplicants, and socket layer
- LWIP integrated to provide native-IP support.
- Support for WINC1500...more soon!



## Highlights: Challenges Managing Connected Products

Cross-platform Support



- Well-defined drivers and HAL interfaces
- Build and package management system optimized to understand BSP and link options

Software Upgrade



- Build tool creates signed images
- Image download over Serial, BLE and Wi-Fi
- Bootloader verifies SHA-256/RSA/ECC-DSA signature

Debugging



- Consistent logging and statistics infrastructure
- Core dumps
- Kernel-level support: sanity, stack guards, memory tracking

Power Mgmt



- Hard sleep and wakeup support (low RAM states)
- Tickless 'idle' and driver suspend
- Networking stack sleep management



#### Build, Package, and Project Management: newt tool

- Composable System
  - Open-source project collaboration
  - Maintaining private code trees
  - Enforced source code layout



- Install and Upgrade
- Unified method for #includes and #defines
- Dependencies and APIs

- Build
  - Configuration
  - Multiple targets
  - Source code layout
    - 3<sup>rd</sup> party SDKs



- System configuration (a la Device Tree)
- Toolchains
- Target management
- SDK compilation rules

- Package Management
  - Versioning and stability



- Go Small or Go BIG
- VCS Versioning
- Versioning Scheme (major, minor, rev)
- Tracking branches



#### Build and Package Management: newt tool (continued)

- Artifacts
  - Debugger maintenance
  - Generation of flash images, upgradable images



- bin/ directory, with object files
- Multiple targets stored simultaneously
- Compiler definitions, map files

- Introspection
  - Size
  - Packages
  - Versions



- Display dependencies
- Search for functionality
- Versions installed tracking branches

- Enforced Hierarchy
  - HW: MCU, BSP
  - APP



- BSP + App = BUILD
- BSP -> MCU definition

- System Definition
  - Split images for upgrade
  - RAM locations
  - Flash



- Linker sections defined by system
- Tie-in with flash layout



## Highlight: Apache Mynewt Security

Unique device identification Provisioning Certificate Management Prevent counterfeiting Signed firmware images (newt tool) Upgrade Secure bootloader Leverage either BLE or DTLS security Communications RBAC for commands based upon identity Encrypted flash storage Data and Tamper **TPM/Smart Card aware** Support hardware key access

Designed for security from the ground-up



## Hardware Platform Support

Nordic Semiconductor nRF51/52

- Suitable for single-chip / host BLE designs
- Controller-only operation with host processor
- Cost-effective nRF51, powerful nRF52

ST Micro STM32F/L34

- Host processor, popular for BLE + Wi-Fi
- Offload processor for DSP processing
- Extensive, quality peripheral support
- L\* series provides reduced power consumption

Atmel SAMD/L21



- Extensive low power modes and operation
- Rich set of peripherals
- Community-supported: Arduino Zero series

More Coming Soon!

Cross platform support provides flexibility and price leverage



#### What's Next?

- More boards(!) and processors(!)
  - PIC32MZ (MIPS underway), NXP FRDM-K64, NXP KW41Z, STM32 L4
- Wireless
  - Bluetooth 5 and Bluetooth Mesh
  - LPWA
  - Improved Wi-Fi
- Wired: Ethernet
- Sensor APIs and Sensor Management
- You Decide!







- More information: <a href="http://mynewt.apache.org/">http://mynewt.apache.org/</a>
- Join the development, subscribe to dev@ list.
- Contributors welcome!

## **THANK YOU**

